

Market Report: PSiP & PwrSoC – The Power Density Challenge Continues 2015 – 2021

Now with product and technology details from LTEC

Collaborating with LTEC Corporation, Anagenesis is augmenting its forecast/technology report with LTEC's device and construction benchmarking which provides deeper insight into the micro-construction of present and potential PSiPs & PwrSoCs.

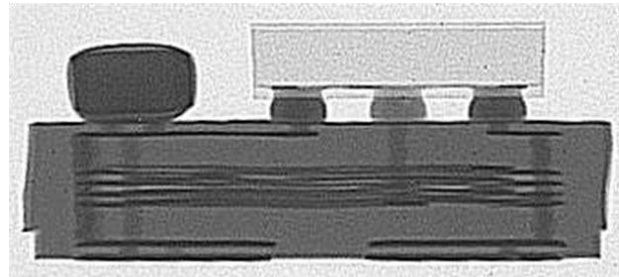
Power Supply in a Package (PSiP)

Encapsulated PSiPs continue to experience double digit growth capturing enough significance to shrink the open PC Board design share of market.

Demand for higher density, low noise solutions will feed growth for 2018 and beyond.

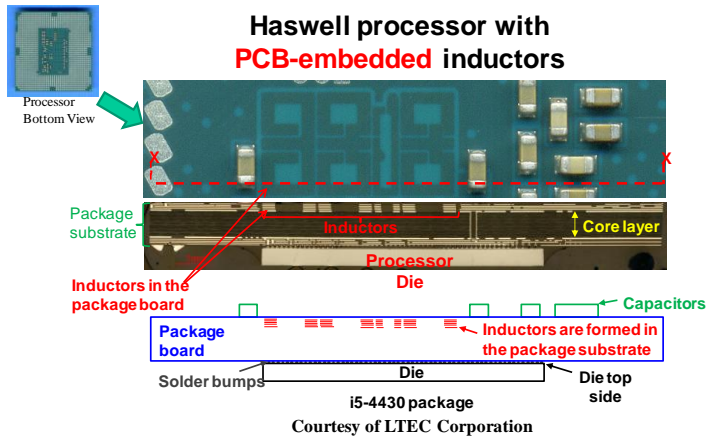
Mergers and acquisitions have created great opportunities for competitive players.

Magnetic designs are meeting customer demand for lower PSiP profiles.



X-ray of side of the PSiP

Courtesy of LTEC Corporation



Courtesy of LTEC Corporation

Power Supply on a Chip (PwrSoC)

While granular technology had a bold commercial launch in 2014, where are they today?

Who will be first to have a sustainable commercial modular PwrSoC? Do L-based or C-based topologies have a stronger future?

Are mergers and acquisitions helping or hindering successful advancement?

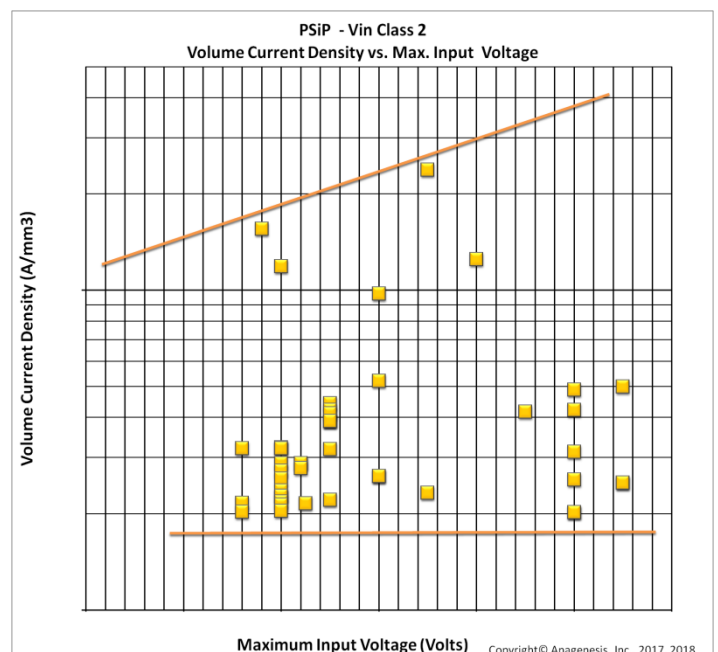
Will all PwrSoC opportunities be captive?

What impact will embedded granular PwrSoC have on external POLs?

Why read this report

Cut through all the buzz and hype surrounding on PSiP and PwrSoC. Get the focused perspective and reality check needed to understand these products and their dynamic markets.

- Save time with this unique technical-marketing tool
- Appraise the technical readiness of 33 companies
- Analyze ten-year trends of over 351 products
- Assess influencing technologies & applications



Learn

- What are the future growth drivers and high growth applications?
 - What is so challenging about successful entry into the PSiP market
 - What technologies will enable the next generation PwrSoC and PSiP?
 - Who are the successful PSiP players and who will be forthcoming?
 - Why are the miniature open construction PSiPs gaining so much growth?
 - What makes the PwrSoC opportunity different from PSiP?
 - How is PwrSoC more than an engineer's dream in search of a market?
 - Why has it been so difficult for the PwrSoC to become a reality?
 - How fast will the PwrSoC market grow and will it continue to drive additional investments?
 - Where do your best emerging opportunities exist?
-

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Future Enabling Technologies

Control Method, Semiconductor Switches, Integrated Magnetics, Integrated Capacitors, and Packaging

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To the Manufacturers, Users, Component Suppliers

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The Anagenesis/LTEC Difference

Anagenesis has investigated and reported the evolving PSiP and PwrSoC markets since 2007. Such long-time, in-depth research qualifies Anagenesis to offer a unique objective perspective. Moreover, we have the engineering expertise and business relationships to take the PSiP and PwrSoC customers' perspective. As a result, previous customers say our reports are "deeply impressive" and "have details unavailable anywhere else."

LTEC Corporation is Japan's leading, well-respected supplier of products and technologies teardown and deep analysis (benchmarking) for IP protection and new product R&D for over three decades. LTEC offers teardowns, deep analysis of competing products and technologies for product development, patents' enforcement and defense with headquarters in Osaka, Japan, and local offices in the USA and the Asia-Pacific region.



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